

NVIC, EXTI and RCC

Introduction:

This laboratory will require you setting up Reset clock controller, nested vectored interrupt controller, external interrupt and event controller. You are required to keep laboratory **logbook**. It is for keeping notes, results, analysis other work. This includes a laboratory **worksheet** that should be completed and fixed into the logbook. You may also wish to include screen capture of programs and results to clarify your results. Work through the activities at your own pace, recording what you do in your logbook and referring to the worksheet, completing the tasks.

Aim:

To gain familiarity with the RCC and PWR configuration on STM32L476VG-DISCO board.

Equipment and sundries:

Hardware:

- 1- STM32L476VG-DISCO
- 2- Cable Type A to Mini Type B
- 3- Logic Analyzer with cables

Software:

- 1- STM32CubeIDE
- 2- PulseView

Logbook Entries:

- 1- Briefly explain the polling and interrupt process (max 200 words).
- 2- Briefly explain RCC and describe the type of clock sources in figure 1 (max 200 words).

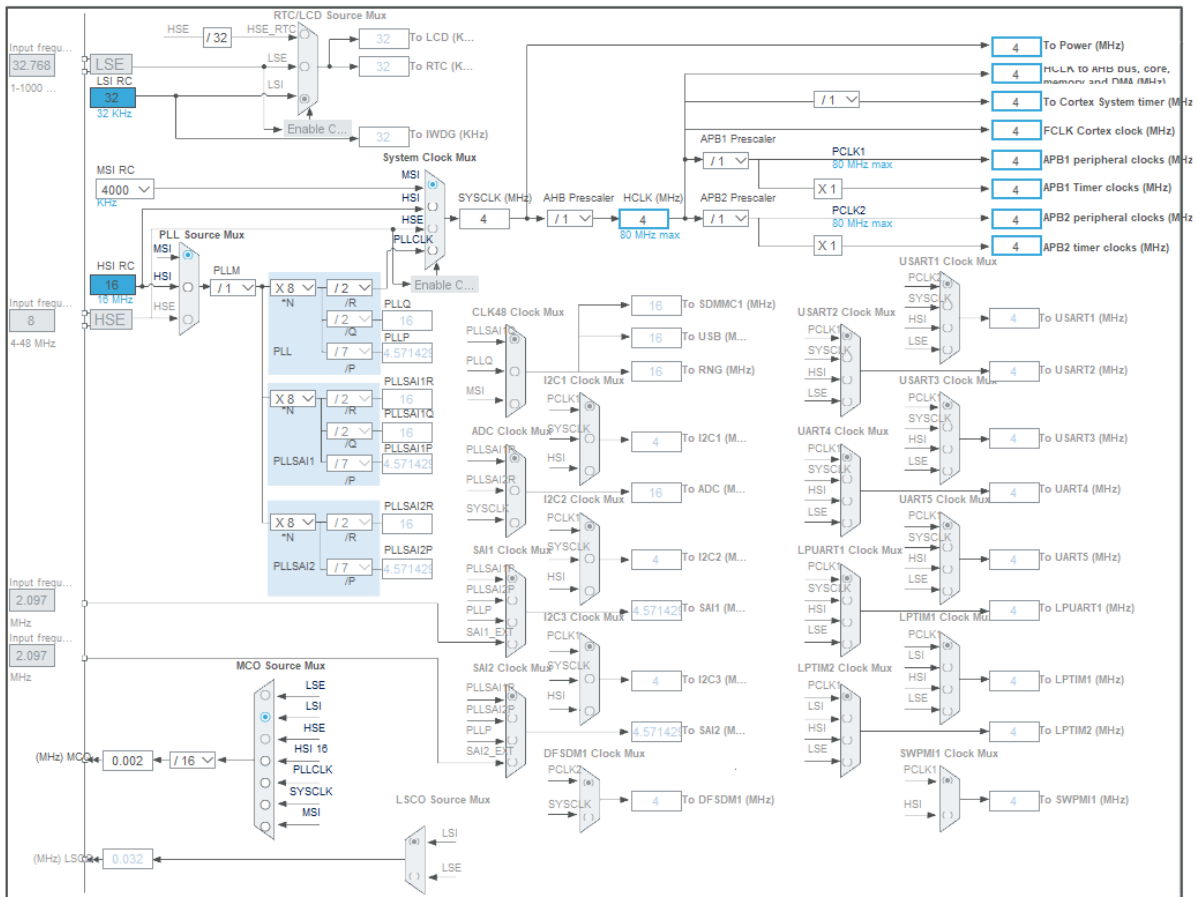


Figure 1: Clock controller STM32CubeIDE

- 3- Document the to do tasks.
- 4- Summaries your work and reflect your learning.